



X-1006-1D US
10/810,034

PATENT
Conf. No.: 8432

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: "A PROGRAMMABLE LOGIC DEVICE WITH
TIME-MULTIPLEXED INTERCONNECT"
Ser. No.: 10/810,034 File Date: March 26, 2004
Examiner: Vibol Tan Art Unit: 2819
Docket No.: X-1006-1D US Conf. No.: 8432

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

Dear Examiner:

In response to the Final Office Action mailed from the Patent Office on October 26, 2005, please amend the above-identified Claims as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.



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Modified 02-03

PTO/SB/21 (01-03)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Application / Conf. No.	10/810,034 / 8432
Filing Date	March 26, 2004
First Named Inventor	Stephen M. Trimberger
Examiner Name	Vibol Tan
Art Unit	2819
Patent No.	
Attorney Docket Number	X-1006-1D US

ENCLOSURES (check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309 (Customer Number)	Reg. Number 37,652
Attn:	Kim Kanzaki	
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Date	December 20, 2005	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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